0210 3/26/99

Docket No. Response To Notice To File Missing Parts Of Application Filing Date Granted (PTO-1533)(Large Entity) 1834-1 In Re Application Of: PARK, Young-woo, et al. Serial No. Filing Date Examiner **Group Art Unit** 09008525 1/16/98 Not Assigned 2811 Method For Manufacturing Semiconductor Memory Device For Preventing Bit Line Oxidation Beer es HUL TO THE ASSISTANT COMMISSIONER FOR PATENTS: **Box Missing Parts** This is a response to the Notice to File Missing Parts of Application - Filing Date Granted (PTO-1533) mailed on 04/15/1998 Enclosed herewith for filing are the following: A copy of the Notice to File Missing Parts of Application - Filing Date Granted (PTO-1533). (REQUIRED) An oath or declaration in compliance with 37 CFR 1.63, including residence information and identifying the application by the above Application Number and Filing Date. A properly signed oath or declaration in compliance with 37 CFR 1.63, identifying the application by the above Application Number and Filing Date. An oath or declaration in compliance with 37 CFR 1.63 listing the names of all inventors and signed by the omitted inventor(s), identifying this application by the above Application Number and Filing Date. ☐ A verified English translation of the non-English language application papers as originally filed. It is requested that this translation be used as the copy for examination purposes in the United States Patent and Trademark Office. ☐ Other (list):

Response To Notice To File Missing Parts Of Application Filing Date Granted (PTO-1533)(Large Entity)			Docket No. 1834-1
In Re Application Of: PARK, Young-woo, et al.			
Serial No. 9008525	Filing Date 1/16/98	Examiner Not Assigned	Group Art Unit 2811
Invention: Method For Manufacturing Semiconductor Memory Device For Preventing Bit Line Oxidation			
TO THE ASSISTANT COMMISSIONER FOR PATENTS:			
TRADEMENT & TRADEMENT	Box Mis	ssing Parts	Para Second
□ Completion of application fees as calculated below:			JUL 0 1 1998
☐ Utility application filing fee			GROUP <u>Broup</u>
☐ Design application filing fee			
☐ Total number of independent claims =			RECEIVED
			JUL 2 4 2001
☐ Total number of claims =		0	FFICE OF PETITIONS
☐ Multiple dependent claims			
Surcharge for late payment of filing fee and/or late filing of original declaration or oath \$130.00			
Petition and fee for filing by other than all the inventors or a person not the invention			
☐ Fee for processing an application filed with a non-English language specification			
☐ Fee for processing and retention of application			
		Total completion of appl	ication fees \$130.00
This is a request under the provisions of 37 CFR 1.136(a) to extend the period for filing a response to the above-identified Notice to File Missing Parts of Application. The requested extension is as follows (check time period desired). If an additional time extension is required, please consider this a petition therefor.			
⊠ One	month	☐ Three months ☐ For	ur month:s
from:	June 15, 1998	until: June 29,	
	Date	Total time ex	
		Т	otal fees due \$240.00

Response To Notice To File Missing Parts Of Application Filing Date Granted (PTO-1533) (Large Entity)

Docket No. 1834-1

In Re Application Of: PARK, Young-woo, et al.

Serial No. 9008525

Filing Date 1/16/98

Examiner Not Assigned

Group Art Unit

2811

Invention: Method For Manufacturing Semiconductor Memory Device For Preventing Bit Line Oxidation



TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Box Missing Parts

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JUL 0 1 1998

GROUP 2100

The fee of

\$240

is to be paid as follows:

- ☐ A check in the amount of the fee is enclosed.
- The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account No. 19-0124

 A duplicate copy of this sheet is enclosed.
- If an additional extension of time is required, please consider this a petition therefor and charge any additional fees which may be required to Deposit Account No. 19-0124
 A duplicate copy of this sheet is enclosed.

RECEIVED

JUL 2 4 2001

OFFICE OF PETITIONS

WWZ Deary, M Signature

Dated: June 29, 1998

William L. Geary, Jr. Reg. No. 35,879 Samsung Electronics 1200 New Hamshire Avenue, N.W. Suite 550 Washington, DC 20036

(202) 887-5666

CC:

I certify that this document and fee is being deposited on with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature of Person Mailing Correspondence

Typed or Printed Name of Person Mailing Correspondence